

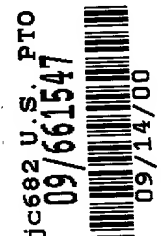


09-18-00

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DOCKET NO. BERG-2456

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Donald K. Harper, Jr.

Serial No.: Not yet assigned

Group Art Unit: Not yet assigned

Filing Date: Filed concurrently herewith Examiner: Not yet assigned

For: HIGH DENSITY CONNECTOR

EXPRESS MAIL LABEL NO: EM695383397US

DATE OF DEPOSIT: September 14, 2000

Box ☒ Patent Application
☐ Provisional ☐ Design

Assistant Commissioner for Patents
Washington DC 20231

Sir:

PATENT APPLICATION TRANSMITTAL LETTER

Transmitted herewith for filing, please find

☒ A Utility Patent Application under 37 C.F.R. 1.53(b).

It is a continuing application, as follows:

☐ continuation ☐ divisional ☐ continuation-in-part of prior application number
____/____.

☐ A Provisional Patent Application under 37 C.F.R. 1.53(c).

☐ A Design Patent Application (submitted in duplicate).

Including the following:

- ☐ Provisional Application Cover Sheet.
- ☒ New or Revised Specification, including pages 1 to 16 containing:
- ☒ Specification
 - ☒ Claims
 - ☒ Abstract
 - ☐ Substitute Specification, including Claims and Abstract.
- ☐ The present application is a continuation application of Application No. _____ filed _____. The present application includes the Specification of the parent application which has been revised in accordance with the amendments filed in the parent application. Since none of those amendments incorporate new matter into the parent application, the present revised Specification also does not include new matter.
- ☐ The present application is a continuation application of Application No. _____ filed _____, which in turn is a continuation-in-part of Application No. _____ filed _____. The present application includes the Specification of the parent application which has been revised in accordance with the amendments filed in the parent application. Although the amendments in the parent C-I-P application may have incorporated new matter, since those are the only revisions included in the present application, the present application includes no new matter in relation to the parent application.
- ☐ A copy of earlier application Serial No. _____ Filed _____, including Specification, Claims and Abstract (pages 1 - @@), to which no new matter has been added TOGETHER WITH a copy of the executed oath or declaration for such earlier application and all drawings and appendices. Such earlier application is hereby incorporated into the present application by reference.
- ☐ Please enter the following amendment to the Specification under the Cross-Reference to Related Applications section (or create such a section) : "This Application:
- ☐ is a continuation of ☐ is a divisional of ☐ claims benefit of U.S. provisional Application Serial No. _____ filed _____.

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- ☐ Signed Statement attached deleting inventor(s) named in the prior application.
- ☐ A Preliminary Amendment.
- ☒ 8 Sheets of ☒ Formal ☐ Informal Drawings.
- ☐ Petition to Accept Photographic Drawings.
- ☐ Petition Fee
- ☒ An ☒ Executed ☐ Unexecuted Declaration or Oath and Power of Attorney.
- ☒ An Associate Power of Attorney.
- ☒ An ☒ Executed ☐ Copy of Executed Assignment of the Invention to BERG Technology, Inc.
- ☒ A Recordation Form Cover Sheet.
- ☒ Authorization to charge Recordation Fee of \$40.00 to Deposit Account.
- ☐ The prior application is assigned of record to _____
- ☐ Priority is claimed under 35 U.S.C. § 119 of Patent Application No. _____ filed _____ in _____ (country).
- ☐ A Certified Copy of each of the above applications for which priority is claimed:
- ☐ is enclosed.
- ☐ has been filed in prior application Serial No. _____ filed _____.
- ☐ An ☐ Executed or ☐ Copy of Executed Earlier Statement Claiming Small Entity Status under 37 C.F.R. 1.9 and 1.27
- ☐ is enclosed.
- ☐ has been filed in prior application Serial No. _____ filed _____, said status is still proper and desired in present case.
- ☐ Diskette Containing DNA/Amino Acid Sequence Information.
- ☐ Statement to Support Submission of DNA/Amino Acid Sequence Information.

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☐ The computer readable form in this application _____, is identical with that filed in Application Serial Number _____, filed _____. In accordance with 37 CFR 1.821(e), please use the ☐ first-filed, ☐ last-filed or ☐ only computer readable form filed in that application as the computer readable form for the instant application. It is understood that the Patent and Trademark Office will make the necessary change in application number and filing date for the computer readable form that will be used for the instant application. A paper copy of the Sequence Listing is ☐ included in the originally-filed specification of the instant application, ☐ included in a separately filed preliminary amendment for incorporation into the specification.

☐ Information Disclosure Statement.

☐ Attached Form 1449.

☐ Copies of each of the references listed on the attached Form PTO-1449 are enclosed herewith.

☐ A copy of Petition for Extension of Time as filed in the prior case.

☐ Appended Material as follows: _____

☒ Return Receipt Postcard (should be specifically itemized).

☐ Other as follows: _____

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FEE CALCULATION:

- ☐ Cancel in this application original claims _____ of the prior application before calculating the filing fee. (At least one original independent claim must be retained for filing purposes.)

				SMALL ENTITY		NOT SMALL ENTITY	
				RATE	FEE	RATE	FEE
PROVISIONAL APPLICATION				\$75.00	\$	\$150.00	\$
DESIGN APPLICATION				\$155.00	\$	\$310.00	\$
UTILITY APPLICATIONS BASE FEE				\$345.00	\$	\$690.00	\$690.00
UTILITY APPLICATION; ALL CLAIMS CALCULATED AFTER ENTRY OF ALL AMENDMENTS							
	No. Filed		No. Extra				
TOTAL CLAIMS	25 - 20 =	5		\$9 each	\$	\$18 each	\$90.00
INDEP. CLAIMS	4 - 3 =	1		\$39 each	\$	\$78 each	\$78.00
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				\$130	\$	\$260	\$
ADDITIONAL FILING FEE					\$		\$
TOTAL FILING FEE DUE					\$		\$858.00

- ☐ A Check is enclosed in the amount of \$_____.
- ☒ The Commissioner is authorized to charge payment of the following fees and to refund any overpayment associated with this communication or during the pendency of this application to deposit account 23-3050. This sheet is provided in duplicate.
- ☒ The foregoing amount due.
- ☒ Any additional filing fees required, including fees for the presentation of extra claims under 37 C.F.R. 1.16.
- ☒ Any additional patent application processing fees under 37 C.F.R. 1.17 or 1.20(d).
- ☐ The issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance.

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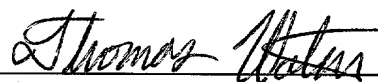
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☐ The Commissioner is hereby requested to grant an extension of time for the appropriate length of time, should one be necessary, in connection with this filing or any future filing submitted to the U.S. Patent and Trademark Office in the above-identified application during the pendency of this application. The Commissioner is further authorized to charge any fees related to any such extension of time to deposit account 23-3050. This sheet is provided in duplicate.

SHOULD ANY DEFICIENCIES APPEAR with respect to this application, including deficiencies in payment of fees, missing parts of the application or otherwise, the United States Patent and Trademark Office is respectfully requested to promptly notify the undersigned.

Date:

9/14/00



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HIGH DENSITY CONNECTOR

Background of the Invention

1. Field of the Invention:

The present invention relates to electrical connectors and more particularly to high I/O density connectors such as connectors that are attachable to a circuit substrate or electrical component by use of a fusible element, such as a solder ball contact surface.

2. Brief Description of Prior Developments:

The drive to reduce the size of electronic equipment, particularly personal portable devices and, to add additional functions to such equipment has resulted in an ongoing drive for miniaturization of all components. Miniaturization efforts have been especially prevalent in the design of electrical connectors. Efforts to miniaturize electrical connectors have included reductions in the pitch between terminals in single or double row linear connectors, so that a relatively high number of I/O or other signals can be interconnected within tightly circumscribed areas allotted for receiving connectors. The drive for miniaturization has also been accompanied by a shift in manufacturing preference to surface mount techniques (SMT) for mounting components on circuit substrates. The confluence of the increasing use of SMT and the requirement for fine pitch has resulted in designs approaching the high volume, low cost limits of SMT. The SMT limit is being reached because further reductions in pitch greatly increase the risk of electrical bridging between adjacent solder pads or terminals during reflow of the solder paste.

To satisfy the need for increased I/O density, electrical connectors have been proposed having a two dimensional array of terminals. Such designs can provide improved density. However, these connectors present certain difficulties with respect to attachment to the circuit substrate using SMT because the surface mount tails of most, if not all, of the

terminals must be attached beneath the connector body. As a result, the use of two-dimensional array connectors requires mounting techniques that are highly reliable because of the difficulty in visually inspecting the solder connections and repairing them, if faulty.

Moreover, high terminal pin densities have made terminal pin soldering more difficult, particularly in SMT if there is a lack of coplanarity between the connector and the printed circuit board. In such a situation, some of the solder joints between the terminal pins and the PCB may not be satisfactory. As a result, reliability of the connector to circuit board connection may suffer.

Floating terminal pins have been proposed to allow the connector to adjust to any irregularities between the planarity of the connector and the circuit board. Some floating terminal pins have used a through hole in the connector body with a diameter about the size of the main terminal pin. However, because the through hole has to accommodate both the terminal pin and a stop that is typically pushed into the through hole during assembly, such designs can have dimensional tolerances that present manufacturing difficulties.

Other mounting techniques for electronic components have addressed the reliability of solder connections in hard to inspect positions. For example, integrated circuit (IC) mounting to plastic or ceramic substrates, such as a PCB, have increasingly employed solder balls and other similar packages to provide a reliable attachment. In the solder ball technique, spherical solder balls attached to the IC package are positioned on electrical contact pads formed on a circuit substrate to which a layer of solder paste has been applied, typically by use of a screen or mask. The assembly is then heated to a temperature at which the solder paste and at least a portion of the solder ball melt and fuse to the contact. This heating process is commonly referred to as solder reflow. The IC is thereby connected to the substrate without need of external leads on the IC.

While the use of solder balls in connecting electrical components, such as ICs, directly to a substrate has many advantages, some flexibility is lost. For example, for electrical components or ICs that are replaced or upgraded, removal and reattachment can be a burdensome process, since generally the solder connection must be reheated to remove the electrical component. The substrate surface must then be cleaned and prepared anew for the

replacement electrical component. This is especially troublesome when the overall product containing the electrical component is no longer in the control of the manufacturer, i.e., the product must be returned, or a field employee must visit the product site in order to replace the component.

5 Of additional concern is thermally induced stress resulting from the effects of differential Coefficients of Thermal Expansion (CTE) between the electrical component and the circuit substrate. This susceptibility is primarily due to size, material composition and geometrical differences between an electrical component, such as an IC, and a circuit substrate.

10 Today's ICs, e.g., can perform millions of operations per second. Each operation by itself produces little heat, but in the aggregate an IC will heat and cool relative to the surface substrate. The stressful effect on the solder joints can be severe due to the differences in CTE between an electrical component and a circuit substrate. Even if the amount of heat generated at the interface portion between the substrate and electrical component remained relatively constant, differences in size, thickness and material of the
15 substrate will generally cause the substrate and the electrical component to expand or contract at different rates. Further, nonlinearity in the rate of change of thermal expansion (or contraction) at different temperatures can further emphasize differences in CTE. These differences in expansion rates or contraction rates can place a burdensome stress on the solder joint, and consequently, an electrical component otherwise properly attached to a circuit
20 substrate may still be susceptible to solder joint failure due to stress from varying CTEs.

 This is of particular concern for ball type solder connections since the attachment surfaces are relatively small. Additionally, a circuit or wiring board can be very large relative to the size of a component. As a result, the effects from differences in CTE
25 between components can be amplified. Further, since there is no additional mechanical structure, e.g. a pin, for added support, the stress on a solder joint is more likely to cause an electrical connection to fail, resulting in quality problems or rendering the electrical component inoperable. This phenomena is sometimes termed CTE mismatch, referring to the reliability and thus performance of electrical connections. The greater the differential

displacements created by CTE mismatch, the greater is the concern for the electrical integrity of a system. Notwithstanding some loss in flexibility and difficulties due to differences in CTEs, the use of BGA and similar systems in connecting an IC to a substrate has many advantages.

5 In relation to BGA connectors, it is also important that the substrate-engaging surfaces of the solder balls be coplanar to form a substantially flat mounting interface, so that in the final application the balls will reflow and solder evenly to a planar printed circuit board substrate. Any significant differences in solder coplanarity to a given substrate can cause poor soldering performance when the connector is reflowed. To achieve high soldering reliability,

10 Coplanarity of the solder balls is influenced by the size of the solder ball and its positioning on the connector. The final size of the ball is dependent on the total volume of solder initially available in both the solder paste and the solder balls. In applying solder balls to a connector contact, this consideration presents particular challenges because variations in the volume of the connector contact received within the solder mass affect the potential variability of the size
15 of the solder mass and therefore the coplanarity of the solder balls on the connector along the mounting interface.

20 BGA connectors have also been provided for connecting a first substrate or PCB to a second substrate or PCB, thereby electrically connecting the attached electrical components. For example, it has been proposed to secure half of a connector having a grid array of solder conductive portions to a first substrate by way of solder ball reflow, and by securing the other half of the connector having a grid array of solder conductive portions to a second substrate by way of solder ball reflow. This intermediate connector can absorb
25 differences in CTE between the first and second substrate. Gains in manufacturing flexibility are also realized since the second substrate, with electrical component(s) attached thereto, can be removed and replaced easily. Since the second substrate is thus removable, it can be sized to match the electrical component. In this manner, CTE mismatch between the second substrate and the electrical component can be minimized.

However, even with the above described intermediate connector, it would be still further advantageous to provide a more flexible vehicle for electrically attaching an electrical component to a substrate that does not require replacing an entire second substrate, or that does not employ a second substrate at all, saving manufacturing time and materials.

Thus, there remains a need for an improved and more flexible apparatus and method for connecting an electrical component to a substrate that addresses the shortcomings of present electrical component connections, and also addresses the need to minimize or decrease CTE mismatch between an electrical component and a substrate.

Summary of the Invention

An improved and more flexible connector assembly and method are provided for connecting an electrical component to a substrate, such as a printed circuit board (PCB), by attaching an electrical component having ball or column grid array solder portions to corresponding electrical contact surfaces of a second connector half, mating first and second connector halves and attaching the first connector half having ball or column grid array solder portions to corresponding electrical contact surfaces of the substrate. The first and second connector halves may be electrically connected to each other via conventional mating techniques. When mated, electrical communication is achieved between corresponding portions of the first and second connector halves. Effects of CTE mismatch are minimized by providing the first and second connector halves between the electrical component and substrate.

Detailed Description of the Drawings

The apparatus assembly and method of the present invention are further described with reference to the accompanying drawings in which:

Fig. 1 is a side view illustration of a first connector half with ball type contact portions, a substrate on which the first connector half is to be mounted, an electrical component or other similar component having ball type contact portions, and a second

connector half on which the electrical component is to be mounted in accordance with the present invention.

Fig. 2 is a perspective view illustration of a first connector half with ball type contact portions, a substrate on which the first connector half is to be mounted, an electrical component or other similar component having ball type contact portions, and a second connector half on which the electrical component is to be mounted in accordance with the present invention.

Fig. 3 is an isolated view illustration of a first connector half with ball type contact portions, a substrate on which the first connector half is to be mounted, an electrical component or other similar component having ball type contact portions, and a second connector half on which the electrical component is to be mounted in accordance with the present invention.

Fig. 4 is an illustration of an element having ball type contact portions in accordance with the present invention.

Figs. 5A through 5C are illustrations of alternate embodiments for connector mating portions in accordance with the present invention.

Fig. 6 is an illustration of alternative grid array contact portions that may be utilized in accordance with the present invention.

Detailed Description of Preferred Embodiments

Use of the present invention involves four components: an electrical device, a first connector half, a second connector half and a substrate. The electrical device has a ball or column grid array system or other type solder portions that attach to the first connector half upon reflow. The first connector half is matable to a second connector half. The second connector half is electrically connected to a substrate via ball or column grid array systems or other type solder portions. The first and second connector halves form a connector when mated, and any type of connector, such as an array connector may be utilized.

Referring to Figs. 1 through 3, the component to connector to substrate assembly includes a first connector half 200, such as an array connector half having fusible

elements such as ball type contact portions 110a, a substrate 400, such as a PCB, on which the first connector half 200 is to be mounted, an electrical device 500 or other similar component having fusible elements such as ball type contact portions 110b, and a second connector half 300 on which the electrical device 500 is to be mounted. The electrical device 500 may be attached to the body of the second connector half 300 by solder reflow of the array of ball type contact portions 110b onto a corresponding array of contacts 309. The body of contacts 309 have mating portions 310 and mounting regions 330. The mounting regions 330 preferably reside within a recess 331 in the bottom of connector 300.

The second connector half 300 mates with the first connector half 200 via the insertion of pin or blade portions 310 into receptacle contacts 210. However, contact portions 210 and 310 may be any type of matable connector contact portions. As shown in the exemplary embodiment, first contact portions 210 are dual beams (Fig. 3) and second contact portions 310 are blades. Contact mounting regions 330, while depicted in Fig. 4 as a straight tail, may be variously formed to provide electrical contact between contact portions 310 and ball type contact portions 110b. For example, contact portions 310 may extend above the surface of a contact mounting region 330 for connection to ball type contact portions 110b after reflow or the tail could be a tab bent to a portion parallel to device 500.

The first connector half 200 includes an array of fusible elements such as ball type contact portions 110a that may be attached to substrate 400 by solder reflow. Connector half 200 also includes an array of dual beam contacts 210 that mate with corresponding contact portions 310. The substrate 400 has an array of solder pads 410 corresponding to the array of ball type contact portions 110a. When connector half 200 is placed on substrate 400, an electrical connection may be made via solder reflow between the ball type contact portions 110a and contacts 410 since in conventional applications, component 500 would directly mount to substrate 400.

Thus, in accordance with the present invention, the connector halves 200 and 300 may be mated together forming an electrical connection between the component 500 and the substrate 400. Use of this novel assembly has the added benefit that the connector halves

absorb differentials in CTEs between the component 500 and substrate 400 since in conventional applications, component 500 would directly mount to substrate 400.

As shown in more detail in the isolated view of Figure 3, solder ball 110b of electrical device 500 is adapted to attach to contact 330 of second connector half 300 by way of solder reflow. Solder ball 110a of first connector half 200 is adapted to be connected to the contact region 410 of substrate 400 by way of solder reflow as well. Subsequently, second contact portion 310 is mated to first contact portion 210.

Typically, the mating between connector halves 200 and 300 is achieved by inserting contact portion 310 between fingers 210a and 210b. The substantially straight elongated connector portion 310 pushes elongated connector portions 210a and 210b away from one another in a direction substantially orthogonal to the mating direction, thereby spring biasing the connecting portions 210a and 210b against connector portion 310. The spring biasing and wiping action during insertion helps bolster the electrical integrity of the electrical connection. Contact portions 210a and 210b can have any configuration suitable for establishing an electrical connection. For example, they may have a curved "S" or double "C" shape. Moreover, portions 210a and 210b may be formed from a single piece of contact material, although separate pieces can be placed together.

In this fashion, CTE mismatch problems due to differences in size and material composition between a component 500 and a substrate 400 can be avoided. The bodies 200 and 300 of the connector provide a middle ground, in effect, to spread out any mismatch that may exist over a greater distance and over more pliant or flexible materials, less prone to mismatch problems.

Fig. 4 is an illustration of an element having an array of ball type contact portions constructed in accordance with the present invention. As shown on a surface of body 120, contacts 100 are formed for the reception of ball type contact portions 110. A discussion of methods of securing a solder ball to a contact and to a PCB is contained in International Publication number WO 98/15989 (International Application number PCT/US97/18066), the teachings of which are hereby incorporated by reference.

Fig. 5A illustrates an alternate embodiment of contact portions 210. As shown, the contact portion 210 has elongated connector portions 211a and 211b electrically attached to first connector half 200. In Fig. 5A, elongated connector portions 211a and 211b have an outwardly arced or bent shape. Portions 211a and 211b are preferably formed from a single piece of contact material, although separate pieces can also be placed together.

In Fig. 5B, connector portions 210a1 and 210b1 of contact portion 210 are separate elongations with a rounded tip, and are formed from a single piece of contact material. Similarly, in Fig. 5C, connector portions 210a2 and 210b2 of contact portion 210 are separate elongations with a substantially pointed tip, and are formed from the same contact material.

Substantially straight elongated contact portion 310 pushes elongated connector portions 210a and 210b away from one another in a direction substantially orthogonal to the mating direction, thereby causing wiping to occur during insertion and spring biasing the contact portions 210a and 210b against connector portion 310. This spring biasing helps to bolster the electrical integrity of the electrical connection established by the first and second connector halves 200 and 300.

Figure 6 illustrates alternative grid array contact portions on device 500 that may be used in accordance with the present invention. Thus far, ball type contact portions 110 have been described and illustrated. However, many different types of array type contact portions can be used in accordance with the present invention depending on the application for which a component 500 is suited, depending on the materials comprising either the substrate 400 or component 500, or depending on the type of manufacture for the substrate 400 or component 500. Thus, column grid array contact portions 600, ceramic ball grid array contact portions 610, tab ball grid array contact portions 620 and plastic ball grid array contact portions 630 may all be used within the spirit and scope of the present invention.

The fusible contacts 110 on the electrical device 500 and contacts 330 on the second array connector will preferably be a solder ball. It is noted, however, that it may be possible to substitute other fusible materials which have a melting temperature less than the melting temperature of the elements being fused together. The fusible element, such as a

solder ball, can also have a shape other than a sphere. As mentioned, examples include column grid arrays 600, ceramic ball grid arrays 610, tab ball grid arrays 620 and plastic ball grid arrays 630.

When the conductive or fusible element is solder, it will preferably be an alloy which is in the range of about 10% Sn and 90% Pb to about 90% Sn and 10% Pb. More preferably the alloy will be eutectic which is 63% Sn and 37% Pb and has a melting point of 183°C. Typically, a "hard" solder alloy with a higher lead content would be used for mating materials such as ceramics. A "hard" contact will "mushroom" or deform slightly as it softens. A "soft" eutectic ball reflows and reforms at lower temperatures. Other solders known to be suitable for electronic purposes are also believed to be acceptable for use in this method. Such solders include, without limitation, electronically acceptable tin-antimony, tin-silver and lead silver alloys and indium. Before the conductive element is positioned in a recess, that recess is usually filled with a solder paste.

While it is believed that a solder paste or cream incorporating any conventional organic or inorganic solder flux may be adapted for use in this method, a so-called "no clean" solder paste or cream is preferred. Such solder pastes or creams would include a solder alloy in the form of a fine powder suspended in a suitable fluxing material. This powder will ordinarily be an alloy and not a mixture of constituents. The ratio of solder to flux will ordinarily be high and in the range of 80% - 95% by weight solder or approximately 50% by volume. A solder cream will be formed when the solder material is suspended in a rosin flux. Preferably the rosin flux will be a white rosin or a low activity rosin flux, although for various purposes activated or superactivated rosins may be used. A solder paste will be formed when a solder alloy in the form of a fine powder is suspended in an organic acid flux or an inorganic acid flux. Such organic acids may be selected from lactic, oleic, stearic, phthalic, citric or other similar acids. Such inorganic acids may be selected from hydrochloric, hydrofluoric and orthophosphoric acid. Cream or paste may be applied by brushing, screening, or extruding onto the surface which may advantageously have been gradually preheated to ensure good wetting.

Heating or solder reflow is preferably conducted in a panel infra red (IR) solder reflow conveyor oven. The components with solder portions would then be heated to a temperature above the melting point of the solder within the solder paste.

While the present invention has been described in connection with the preferred embodiments of the various figures, it is to be understood that other similar embodiments may be used or modifications and additions may be made to the described embodiment for performing the same function of the present invention without deviating therefrom. It will be appreciated by those of ordinary skill in the art that the description given herein with respect to those Figures is for exemplary purposes only and is not intended in any way to limit the scope of the invention.

For example, an electrical connector is described herein having a substantially square or rectangular mounting surface. However, the particular dimensions and shapes of connectors shown and described are merely for the purpose of illustration and are not intended to be limiting. The concepts disclosed herein have a broader application to a much wider variation of connector mounting surface geometries. The concepts disclosed with reference to this connector assembly could be employed, for example, with a connector having a connection mounting surface having a more elongated, irregular or radial geometry.

Further, the first and second connector halves are described with reference to an array of plug contact mating ends 310 on the second connector half 300 being insertable into an array of corresponding dual-pronged receptacle mating portions 210 on the first connector half 200 to achieve electrical communication between the first and second connector halves. However, a variety of pin to receptacle implementations are available for use, and could be employed in the present invention to achieve electrical communication by inserting the first connector half into the second connector half, or vice versa. Further, the first connector half elongated portions 210a and 210b are interchangeable with the second connector half elongated portion 310 and vice versa. Therefore, the present invention should not be limited to any single embodiment, but rather construed in breadth and scope in accordance with the recitation of the appended claims.

WHAT IS CLAIMED IS:

1. An electrical connector assembly adapted for forming a mechanical and an electrical connection between a component and a substrate, said connector assembly comprising:

5 a first connector half having a first array of mating elements and adapted for connection to said substrate; and

a second connector half, for mating with said first connector half, said second connector half having a second array of mating elements and adapted for connection with said component;

10 whereby mating said first and second connector halves electrically connects said component to said substrate.

2. An electrical connector assembly as recited in Claim 1, wherein said first connector half is adapted for connection to said substrate via an array of ball type contact portions on said first connector half that form an electrical connection with an array of electrical contacts on said substrate by way of solder reflow.

3. An electrical connector assembly as recited in Claim 1, wherein said second connector half connects to said component via an array of ball type contact portions on said component that form an electrical connection with an array of electrical contacts on said second connector half by way of solder reflow.

4. An electrical connector assembly as recited in Claim 1, wherein an array of electrical contacts on said substrate corresponds electrically to the an array of ball type contact portions on said first connector half.

5. An electrical connector assembly as recited in Claim 1, wherein an array of ball type contact regions on said first connector half corresponds electrically to the first array of mating elements.

6. An electrical connector assembly as recited in Claim 1, wherein the first array of mating elements corresponds electrically to the second array of mating elements.

7. An electrical connector assembly as recited in Claim 1, wherein the second array of mating elements corresponds electrically to an array of electrical contacts on said second connector half.

8. An electrical connector assembly as recited in Claim 1, wherein an array of electrical contacts on said second connector half corresponds electrically to an array of ball type contact portions on said component.

9. An electrical connector assembly as recited in Claim 1, wherein an array of ball type contact portions on said first connector half is one of a column grid array, ceramic ball grid array, tab ball grid array and plastic ball grid array.

10. An electrical connector assembly as recited in Claim 1, wherein an array of ball type contact portions on said component is one of a column grid array, ceramic ball grid array, tab ball grid array and plastic ball grid array.

11. An electrical connector assembly as recited in Claim 1, wherein a mating element of said first array of mating elements has dual elongations for receiving a single elongation from a mating element of said second array of mating elements.

12. An electrical connector assembly as recited in Claim 1, wherein the first array of mating elements and first array of ball type contact portions are on opposing sides of said first connector half.

13. An electrical connector assembly as recited in Claim 1, wherein the second array of mating elements and an array of electrical contacts on said second connector half are on opposing sides of said second connector half.

5 14. An electrical connector assembly adapted for forming a mechanical and an electrical connection between a component and a connector half, said connector assembly comprising:

a connector half, for mating with another connector half, having an array of mating elements and an array of electrical contacts; and

10 an electrical component having an array of ball type contact portions attached thereto;

15 15. An electrical connector assembly as recited in Claim 14, wherein the array of mating elements and array of electrical contacts are on opposing sides of said connector half.

16. An electrical connector assembly as recited in Claim 14, wherein the array of mating elements corresponds electrically to the array of contact portions.

20 17. An electrical connector assembly as recited in Claim 14, wherein the array of contact portions corresponds electrically to the array of ball type contact portions.

18. An electrical connector assembly as recited in Claim 14, wherein said array of ball type contact portions is one of a column grid array, ceramic ball grid array, tab ball grid array and plastic ball grid array.

25 19. An electrical connector assembly as recited in Claim 14, wherein a mating element of said array of mating elements has dual elongations for receiving a single elongation from a mating element of another connector half.

20. An electrical connector assembly as recited in Claim 14, wherein a mating element of said array of mating elements has a single elongation for insertion between dual elongations of a mating element of another connector half.

5 21. A method of removably attaching an electronic device to a substrate, the electronic device having fusible elements thereon, the method comprising the steps of:
mounting a first connector to the substrate; and
fusing said fusible elements on the electronic device to contacts on a second connector, said second connector mateable with said first connector;

10 wherein the electronic device is removably attached to the substrate without having to reflow the fusible elements.

15 22. The method of claim 21, wherein the fusing step directly fuses said fusible elements to said contacts on said second connector.

23. The method of claim 21, wherein said second connector includes a housing having a recess in which a tail of each of said contacts reside, and the fusing step at least partially occurs in said recess.

20 24. In a ball grid array connector engageable with a mating connector mounted to a substrate, the ball grid array connector having a housing, contacts and fusible elements mounted to said contacts, wherein the improvement comprises said fusible elements being part of an electronic device so that said electronic device can removably attach to said substrate without having to reflow said fusible elements.

25 25. The ball grid array connector as recited in claim 24, wherein said electronic device is one of a column grid array, ceramic ball grid array, tab ball grid array and plastic ball grid array.

Abstract of the Invention

An improved and more flexible connector assembly and method are provided for connecting an electrical component to a substrate, such as a printed circuit board (PCB), by attaching an electrical component having ball or column grid array solder portions to
5 corresponding electrical contact surfaces of a second connector half, mating first and second connector halves and attaching the first connector half having ball or column grid array solder portions to corresponding electrical contact surfaces of the substrate. The first and second connector halves may be electrically connected to each other via conventional mating techniques. When mated, electrical communication is achieved between corresponding
10 portions of the first and second connector halves. Effects of CTE mismatch are minimized by providing the first and second connector halves between the electrical component and substrate.

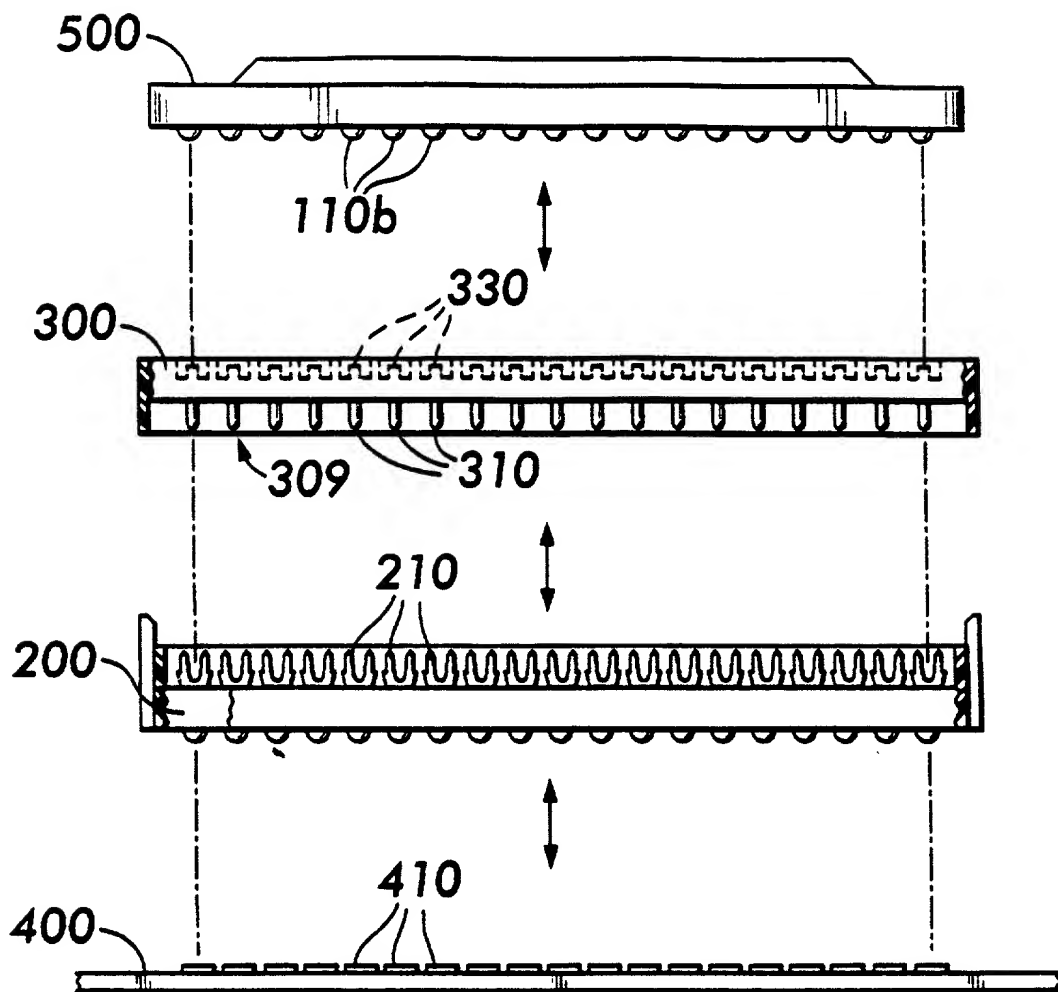


FIG. 1

FIG. 2

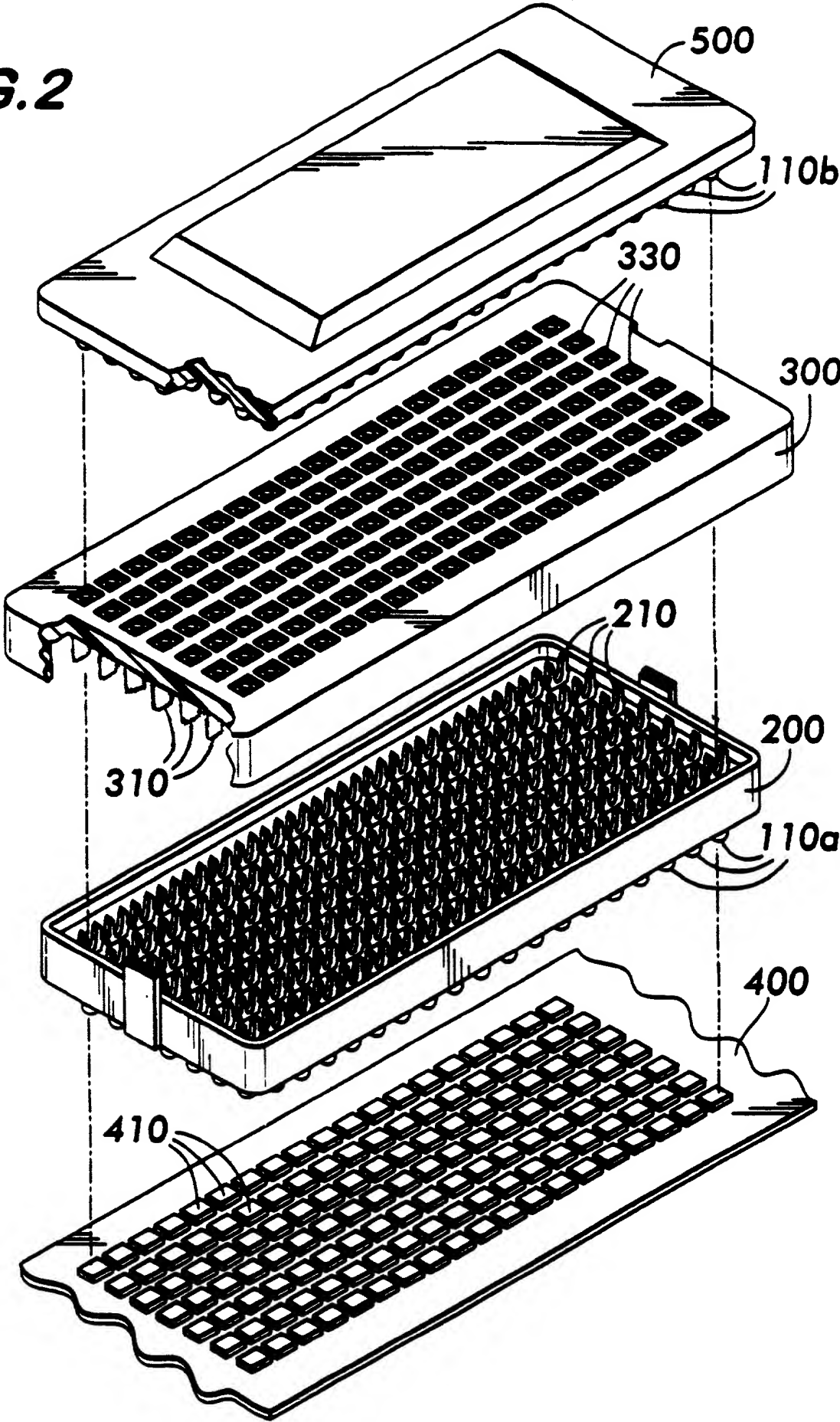
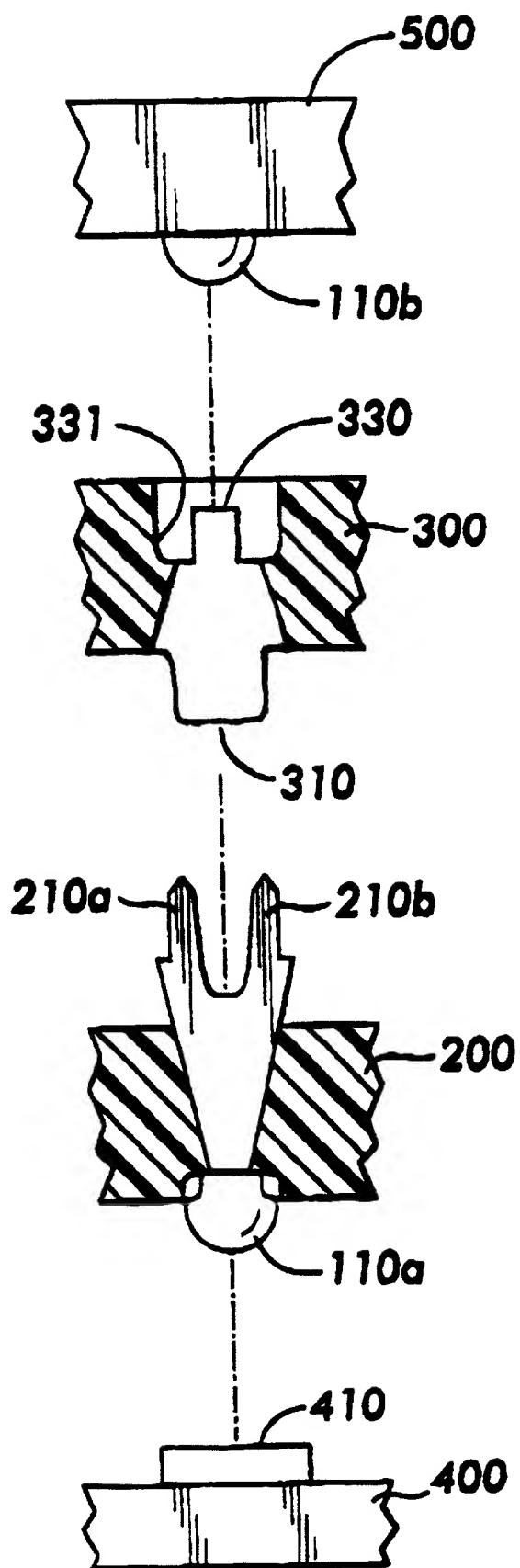


FIG. 3



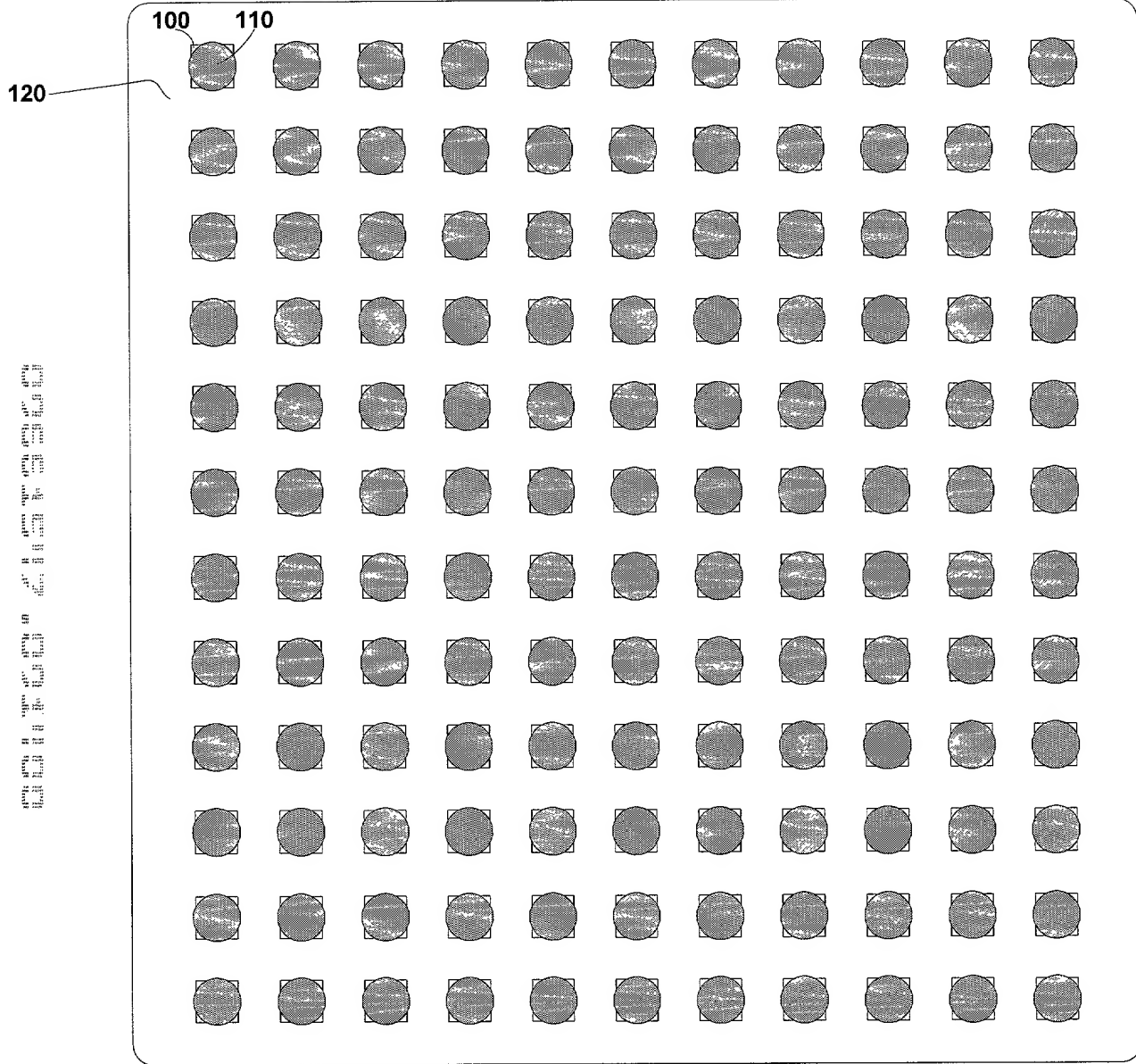


FIG. 4

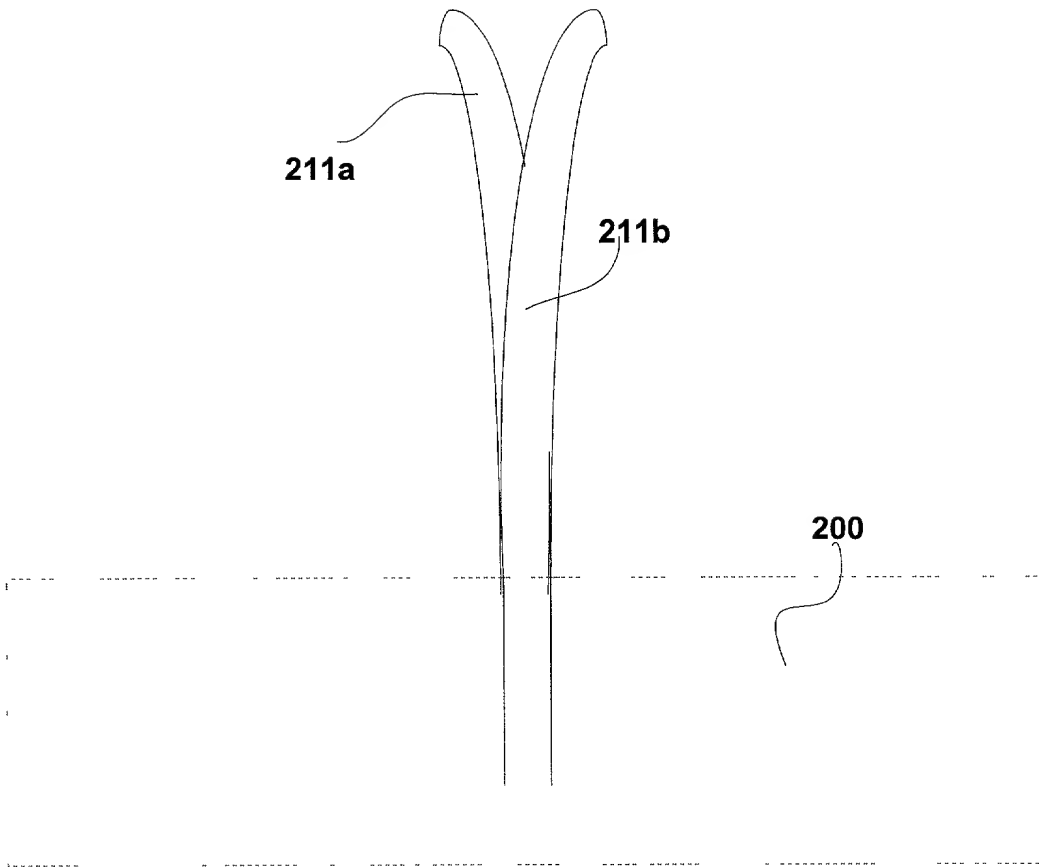


FIG. 5A

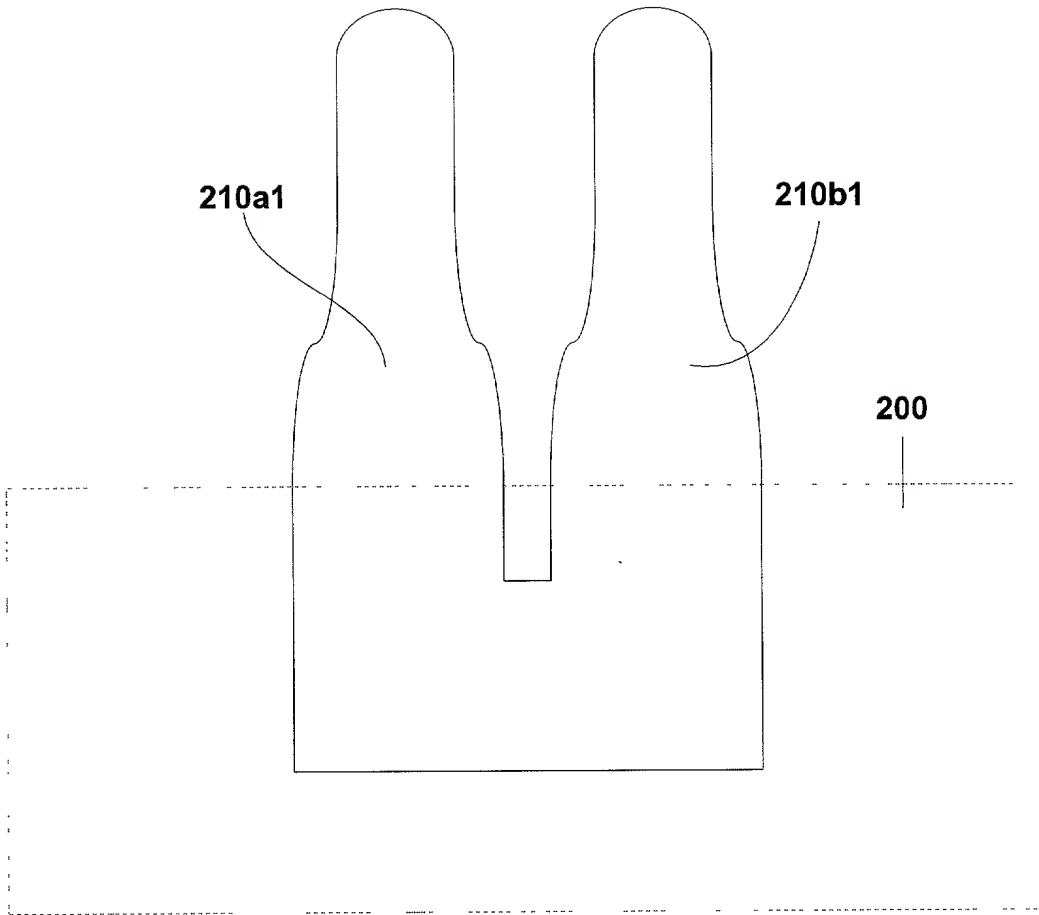


FIG. 5B

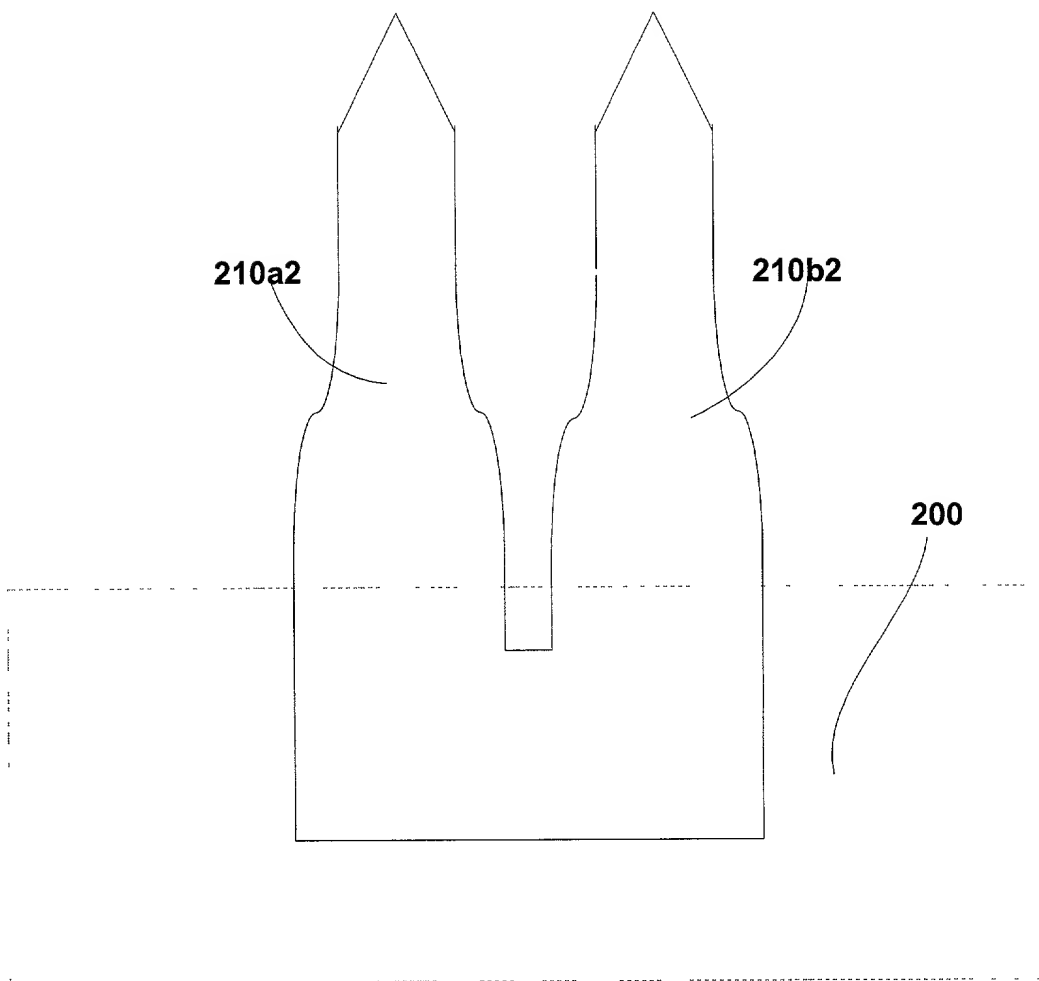


FIG. 5C

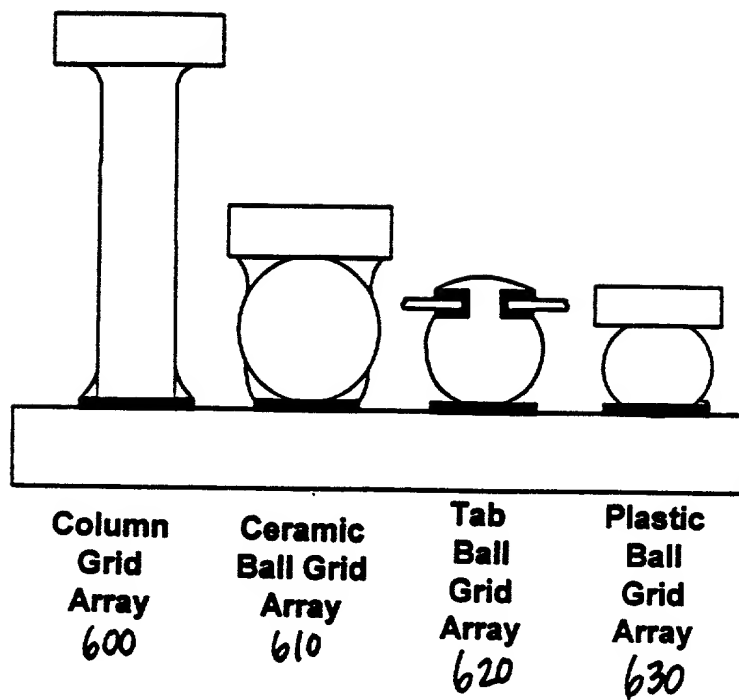


FIG. 6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Donald K. Harper, Jr.

For: HIGH DENSITY CONNECTOR

Group Art Unit: Not Yet
Assigned

Examiner: Not Yet Assigned

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; and

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a

☒ Utility Patent ☐ Design Patent

is sought on the invention, whose title appears above, the specification of which:

- ☒ is attached hereto.
- ☐ was filed on _____ as Serial No. _____.
- ☐ said application having been amended on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to be material to the patentability of this application in accordance with 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a-d) of any **foreign application(s)**

for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of any application on which priority is claimed:

Priority Claimed (If X'd)	Country	Serial Number	Date Filed
<input type="checkbox"/>	_____	_____	_____
<input type="checkbox"/>	_____	_____	_____

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to be material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Serial Number	Date Filed	Patented/Pending/Abandoned
_____	_____	_____
_____	_____	_____
_____	_____	_____
_____	_____	_____

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

Serial Number	Date Filed
_____	_____

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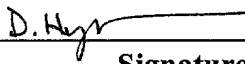
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Mailing Address: 414 N. 48th St. Harrisburg, PA 17111	Date of Signature: <u>9-8-00</u>
City/State of Actual Residence: Harrisburg, PA	Citizenship: <u>United States</u>

DOCKET NO. BERG-2456

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Donald K. Harper, Jr.

Serial No.: Not yet assigned

Group Art Unit: Not yet assigned

Filed: Concurrently herewith

Examiner: Not yet assigned

For: HIGH DENSITY CONNECTOR

Assistant Commissioner for Patents
Washington DC 20231

Sir:

ASSOCIATE POWER OF ATTORNEY

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